

FIG. 1

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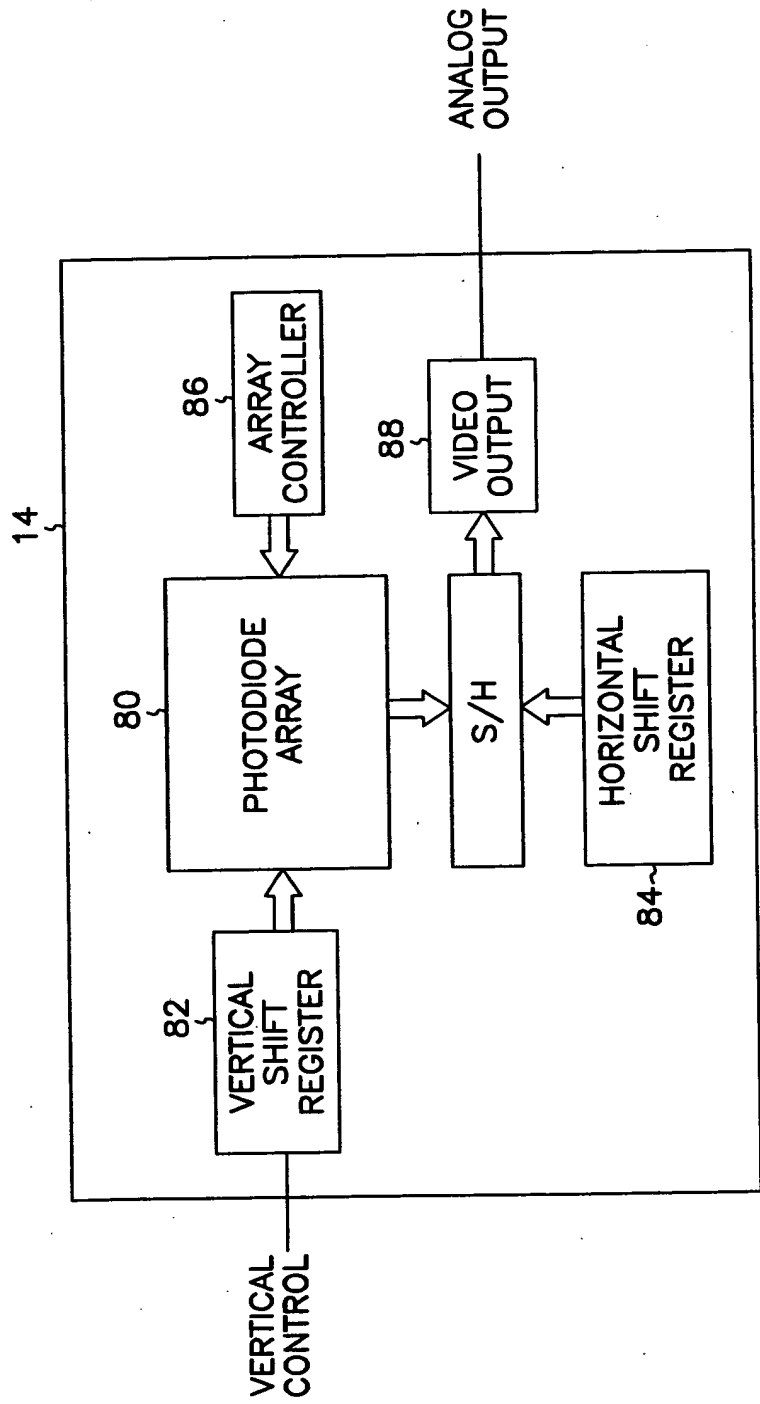
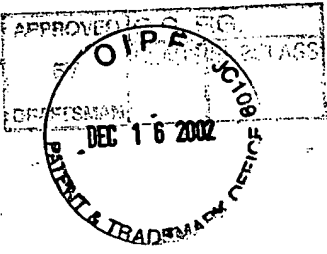
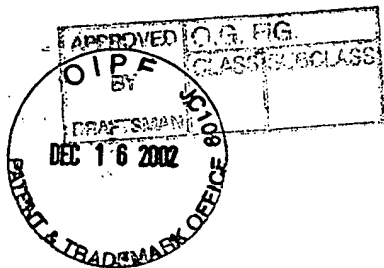


FIG. 2

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CMOS IMAGER WITH INTEGRATED NON-VOLATILE MEMORY  
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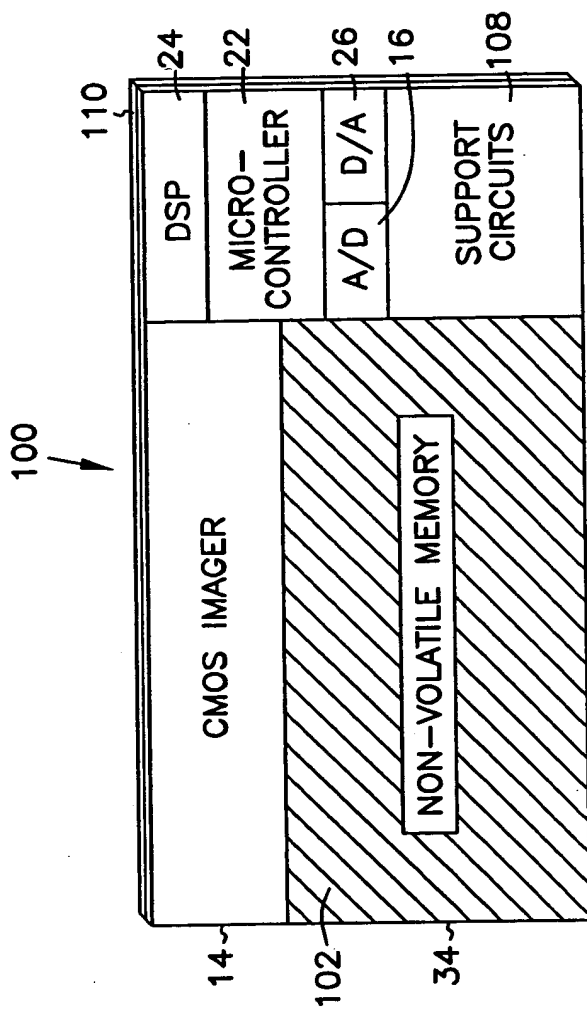


FIG. 3

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